



PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

Tokuroh OZAWA et al.

Group Art Unit:

Application No.: 10/625,617

Examiner: Not Yet Assigned

Filed: July 24, 2003

Docket No.: 100629.03

For: DISPLAY APPARATUS

INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

Sir:

Pursuant to 37 CFR §1.56, the attention of the Patent and Trademark Office is hereby directed to the reference(s) listed on the attached PTO-1449. Unless otherwise indicated herein, one copy of each reference is attached. It is respectfully requested that the information be expressly considered during the prosecution of this application, and that the reference(s) be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

- ☒ 1. This Information Disclosure Statement is being filed (a) within three months of the U.S. filing date of this non-CPA application, OR (b) before the mailing date of a first Office Action on the merits in the present application. No certification or fee is required.
- ☒ 2. The references were cited in a counterpart foreign application. An English language version of the foreign search report is attached for the Examiner's information.
- ☒ 3. English-language Abstracts of the non-English language references are attached hereto.
- ☒ 4. Reference no. 1 is the U.S. counterpart to Japanese reference no. 5 and reference no. 2 is the U.S. counterpart to Japanese reference no. 8.

Respectfully submitted,

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<p>DEPOSIT ACCOUNT USE AUTHORIZATION Please grant any extension necessary for entry; Charge any fee due to our Deposit Account No. 15-0461</p>



Sheet 1 of 1

Form PTO-1449 (REV. 8-83)		US Dept. of Commerce PATENT & TRADEMARK OFFICE		ATTY DOCKET NO. 100629.03		APPLICATION NO. 10/625,617	
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)				APPLICANT(S) Tokuroh OZAWA et al.			
				FILING DATE July 24, 2003		GROUP	
U.S. PATENT DOCUMENTS							
EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUB CLASS	
	1	5,786,796	07/1998	TAKAYAMA et al.	345	76	
	2	5,986,632	11/1999	TAKAYAMA et al.	345	99	
	3	5,463,279	10/1995	KHORMAEI	315	169.3	
FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUB CLASS	
	4	EP 0 653 741 A1	05/1995	EUROPE			
	5	JP 08-241057 (w/ English abstract)	09/1996	JAPAN			
	6	WO 96/06456	02/1996	WIPO			
	7	EP 0 112 700	07/1984	EUROPE			
	8	JP 08-129360 (w/ English abstract)	05/1996	JAPAN			
	9	EP 0 717 439 A2	06/1996	EUROPE			
OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, etc.)							
	10	Lewis A. G. et al.: "Polysilicon TFT Circuit Design and Performance", IEEE Journal of Solid-State Circuits, IEEE Inc. New York, US, Vol. 27, No. 12, 1 December 1992					
	11	Lewis A. G. et al.: "Physical Mechanisms for Short Channel Effects in Polysilicon Thin Film Transistors", Proceedings of the International Electron Devices Meeting. Washington, Dec. 3-6, 1989, New York, IEEE, US					
EXAMINER					DATE CONSIDERED		
Examiner: Initial if citation considered, whether or not citation is in conformance with M.P.E.P. 609; draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.							

Date: October 24, 2003